

WHAT IS CLAIMED IS:

5 1. A control system for controlling an apparatus which includes an actuator that has a movable member and that urges movement of the member in response to an actuator control signal, and which is operable to generate a digital position error signal representing an actual state of the member, said control system comprising:

10 a first control portion responsive to an input signal representing a target position of the member, and operable in response to the input signal to utilize a model reference control technique to generate a digital first control signal representing a movement of the member, and to generate a second control signal representing an expected response of the actuator to the digital first control signal;

15 a second control portion responsive to the digital position error signal, the digital first control signal, and the second control signal, and operable to generate in response thereto a digital third control signal representing a movement of the member;

20 a first digital to analog converter operable to convert the digital first control signal to an analog first control signal;

25 a second digital to analog converter operable to convert the digital third control signal to an analog third control signal; and

30 a junction responsive to the analog first control signal and the analog third control signal, and operable to generate the actuator control signal by combining the analog first control signal and the analog third control

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signal in a manner giving the analog first control signal greater weight than the analog third control signal.

2. A control system according to Claim 1, wherein
5 said first control portion includes:

a model reference of the actuator responsive to a
feedforward control signal representing an operation to be
effected by the actuator in order to place the member in
the target position, and operable to generate a model
10 control signal representing an expected response of the
actuator to the feedforward control signal; and

a model reference control circuit responsive to the
model control signal and the input signal representing the
specified state of the actuator, and operable to generate
15 the feedforward control signal in response to the model
control signal and the input signal, wherein the
feedforward control signal is a digital signal;

the first digital control signal being the feedforward
control signal, and the second digital control signal being
20 the model control signal.

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3. A control system according to Claim 1, wherein said second control portion includes:

a summing arrangement operable to receive the digital first control signal and the digital third control signal, and operable to generate a digital positioning signal by adding the digital first control signal and the digital third control signal in a manner giving the digital first control signal greater weight than the digital third control signal;

a state estimator responsive to the digital positioning signal and the digital position error signal, and operable to generate a state estimation signal representing an estimated state of the actuator;

a summing arrangement responsive to the second control signal and the state estimation signal, and operable to generate a state error signal by subtracting the state estimation signal from the second control signal; and

a control law responsive to receive the state error signal and operable to generate a correction control signal;

the digital third control signal being the correction control signal.

4. A control system according to Claim 1, wherein the second control signal includes information representing an expected position of the member.

5. A control system according to Claim 1, wherein the second control signal includes information representing an expected velocity of the member.

6. A control system according to Claim 1, wherein the second control signal includes information representing an expected acceleration of the member.

7. A control system according to Claim 1, wherein the second control signal includes information representing an expected position, velocity and acceleration of the member.

8. A control system according to Claim 1, wherein the state estimation signal includes information representing an estimated position of the member.

9. A control system according to Claim 1, wherein the state estimation signal includes information representing an estimated velocity of the member.

10. A control system according to Claim 1, wherein the state estimation signal includes information representing an estimated acceleration of the member.

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/ a digital-to-analog converter operable to convert the digital positioning information into the analog positioning signal.

5 12. A control system according to Claim 11, wherein the digital positioning information generated by said digital signal processor includes:

a digital first positioning signal component; and
a digital second positioning signal component; and
wherein said digital-to-analog converter includes:

10 a first digital-to-analog converter operable to convert the digital first positioning signal component into an analog first positioning signal component;

a second digital-to-analog converter operable to convert the digital second positioning signal component into an analog second positioning signal component; and

15 a summing arrangement operable to generate the analog positioning signal by combining the analog first positioning signal component and the analog second positioning signal component in a manner giving the analog first positioning signal component greater weight than the analog second positioning signal component.

20 13. A control system according to Claim 11, further comprising:

25 a power amplifier operable to amplify the analog positioning signal to generate an amplified analog positioning signal which is applied to the actuator.

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14. A control system according to Claim 11, wherein said digital-to-analog converter and said digital signal processor are fabricated in a single piece of semiconductor material.

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15. A control system according to Claim 11, wherein said digital-to-analog converter circuit and said digital signal processor are fabricated in a single piece of semiconductor material which is silicon.

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16. A control system according to Claim 11, wherein said digital signal processor is further operable to utilize a state estimator technique in generating the digital positioning signal.

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17. A control system according to Claim 11, wherein said digital signal processor is further operable to utilize a control law in generating the digital positioning signal.

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18. A control system according to Claim 11, wherein the digital positioning information generated by said digital signal processor includes:

a digital first positioning signal component; and

5 a digital second positioning signal component;

said digital signal processor being operable to utilize said model reference control technique to generate the digital first positioning signal component; and

10 said digital signal processor being operable to utilize a state estimator technique to generate a state estimate signal in response to the digital positioning signal and the digital position error signal, and being operable to utilize a control law technique to generate the digital second positioning signal component in response to the state estimate signal and said model reference control technique;

15 said digital-to-analog converter including:

a first digital-to-analog converter operable to convert the digital first positioning signal component into an analog first positioning signal component;

20 a second digital-to-analog converter operable to convert the digital second positioning signal component into an analog second positioning signal component; and

25 a summing arrangement operable to generate the analog positioning signal by combining the analog first positioning signal component and the analog second positioning signal component in a manner giving the analog first positioning signal component greater weight than the analog second positioning signal component.

19. A method for controlling an apparatus which includes an actuator that has a movable member and that urges movement of the member in response to an actuator control signal, and which is operable to generate a digital position error signal representing an actual state of the member, said method comprising the steps of:

using a model reference control technique responsive to an input signal representing a target position of the member to generate a digital first control signal which represents a movement of the member, and to generate a second control signal which represents an expected response of the actuator to the digital first control signal;

generating a digital third control signal in response to the digital position error signal, the digital first control signal, and the second control signal, the digital third control signal representing a movement of the member;

converting the digital first control signal into an analog first control signal;

converting the digital third control signal into an analog third control signal; and

generating the actuator control signal by combining the analog first control signal and the analog third control signal in a manner giving the analog first control signal greater weight than the analog third control signal.

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20. A method according to Claim 19, wherein said step of using said model reference control technique includes the steps of:

5 generating a model control signal representing an expected state of the actuator in response to a feedforward control signal which represents an operation to be effected by the actuator in order to place the member in a target position; and

10 generating the feedforward control signal in response to the model control signal and the input signal, wherein the feedforward control signal is a digital signal;

15 the digital first control signal being the feedforward control signal, and the second control signal being the model control signal.

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21. A method according to Claim 18, wherein said step of generating the digital third control signal includes the steps of:

generating a digital positioning signal by adding the
5 digital first control signal and the digital third control signal in a manner giving the digital first control signal greater weight than the digital third control signal;

generating in response to the digital positioning signal and the digital position error signal a state
10 estimation signal representing an estimated state of the actuator;

generating a state error signal by subtracting the state estimation signal from the second control signal; and

generating in response to the state error signal a
15 correction control signal which is the digital third control signal.

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